

Office of the Dean Research and Consultancy
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103

Project on: **"MEMS for Electric Machines and Drives (MEMS Machines)"**

[Sponsoring Authority: MeITy, Govt. of India]

Department of Electrical Engineering,
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103

Ref.: Advt. No. EE 1872, dated 06.08.2025

[University Project Code: DRC/MEITY-NAMPET-III(MEMS)[2354]/EE/MS/007/20-21]

Interested candidates are requested to appear for a walk-in interview for the following post in the Department of Electrical Engineering, Indian Institute of Engineering Science & Technology (IEST), Shibpur, Howrah-711103.

Name of the post(s): Research Associate-III.

No of post(s) – One (01)

Essential Qualification(s):

1. Ph. D or equivalent degree in Electrical Engineering with specialization in Power Electronics, Electrical machines and drives from a recognized Indian University/Institute and with at least two research papers in Science Citation Indexed (SCI) journal
- Or
2. ME/ M. Tech with 70% marks or 7.5 CGPA and minimum 5 years of research, teaching and design and development experience with at least three research papers in Science Citation Indexed (SCI) journal.

Desirable Qualification:

1. Proficient in FEM based electromagnetic and coupled circuit simulation.
2. Hands on experience in Power Electronic circuit and machine design, analysis, fabrication and testing.
3. Sound knowledge of vibration, deformation and acoustic noise analysis. (NVH issues)
4. Sound knowledge of open source circuit simulation software like SEQUEL, LT Spice etc.
5. Programming knowledge of DSP, PIC or equivalent in FPGA.

Fellowship amount: Rs. 67,000/- consolidated +30% HRA, as per Govt rules.

Age Limit: As per rules and relaxable for SC/ST/OBC/Woman and physically handicapped candidates, as per Govt norms for Govt. funded R&D projects).

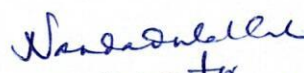
Duration: 3 months or till the termination of the project whichever is earlier, unless notified otherwise by the funding agency. Interested eligible candidates should e-mail soft copies of the application letter in plain paper, with recent bio-data. The above documents and all mark sheets and certificates, in original, shall be verified both before the interview and at the time of joining. The selection will be cancelled if any discrepancies are found in the documents at the time of physical verification. Copies of all documents should accompany the email application. All documents should be self-attested.

Mode, Venue and Date of the interview: (i) Walk in interview (ii) Office of the Department of Electrical Engineering, IEST Shibpur (iii) Date & Time of Interview: **14.08.2025 at 4:00 pm.**

Note:

1. Soft copies of the application letter, bio-data, mark sheets and certificates should be sent through e-mail in advance by 08.08.2025 5:00 PM, to: Prof. Mainak Sengupta, Project Investigator (E-mail id: msg.ee@faculty.iests.ac.in) with copy to nampetproject@gmail.com. Only candidates applying as above shall be considered for the interview on 14.08.2025.
2. No claims for TA/DA shall be entertained.
3. All applications must mention a valid e-mail id and mobile number for communicating.
4. Short listing may be done before the interview, as per rules.

(W. Code DRC-007/25-26)


Dean (R & C) for Dean (R & C)
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